

LSI Logic Corporation 1621 Barber Lane MS: AD-106, Legal Milpitas, CA 95035

	_	September 06, 2		5						
	To: USPTO/Office of Publications				From: Mark Salvatore					
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2005 Under the Par	verwork Reduction Act of the	1995. <u>no decs</u> on	s are required to re	espond to a co	Patent and Ti	Approved rademark ormation (for use ti Office; U. pless it d	hrough 07/31/2005. C	D/SB/21 (09-04) DMB 0651-0031 OF COMMERCE Control number.	
AND STATE OF THE PARTY OF THE P	:		Application Nu	ımber	09/881,584					
TR	ANSMITTAL	-	Filing Date June 14, 200			01				
	FORM		First Named In	nventor	ventor Wilner, Alden R.					
1			Art Unit 2113							
(to be used for	all correspondence after it	nitial filing)	Examiner Name		Baderman	Baderman, Scott T.				
	Pages in This Submission	1	Attorney Dock	et Number	01-076					
			LOSURES	/Ch!	ll that apply					
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Firm Name	LSI Logic Corporation	<u>, </u>	· · · · · · · · · · · · · · · · · · ·	•	· .					
Signature	Loi Zogic Corporation	1 ber								
Printed name	Sandeep Jaggl	/400								
Date	ali		Reg. No.		43,331					
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS, SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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